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Abstract

The purpose of the method according to the invention is to operate multiple parallel-connected pulse-controlled inverters (1, 2). In order to improve this method, the individual current(s) of the pulse-controlled inverters (1, 2), or of a number reduced by 1 (n-1) of pulse-controlled inverters (1, 2), is/are regulated (Figure 1).

[Figures]

pulse-controlled inverter 1

output chokes

output

pulse-controlled inverter 2

Figure 1 Circuit diagram providing an example of a parallel-connected circuit for two single-phase pulse-controlled inverters with IGBTs, freewheeling diodes, and output chokes.

original gate trigger pulses

regulated gate trigger pulses

Figure 2 Gate trigger pulses.

control pulses

modulation pattern

Figure 3 Block diagram of the regulation method.